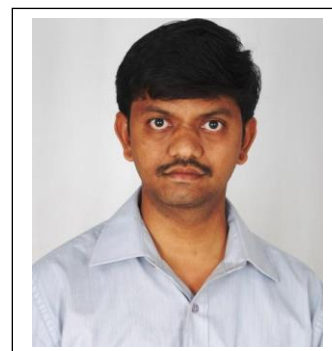


FACULTY PROFILE

1. **Name** : D.AJAY KUMAR
2. **Designation** : Assistant professor
3. **Department** :E.C.E Department
4. **Date of Joining** : 19-06-2010.
5. **Nature of Employment:**Regular
6. **Date of Birth** : 30-03-1984.
7. **Unique ID (AICTE FID)** : 1-435604690
8. **Email** : ajaybabuji@gmail.com
9. **Phone** : 8121209554
10. **Address for communication:** H.No:5-57/2, challavari street,velampet, pentapadu, W.G.Dist., Andhrapadesh-534166.



11. Educational Qualifications:

Qualification	Institute	University/ Board	Specialization	Year of Passing	Division/ Class
Ph.D.	GITAM Institute of technology	GITAM Deemed university	Device modelling		
PG	Bannariamman institute of technology	Anna University	VLSI design	2009	Distinction.
UG	BVCITS, BATLAPALEM	JNTUH	E.C.E	2006	1 st Class
Inter/Diploma	M.R.K. POLYTECHNIC	SBTET	D.E.C.E	2002	1 st Class
SSC	S.T.V.N.H.H.School	SSC		1999	1 st Class

12. Work Experience:

Teaching:11 .3Research: Industry:6 month. Total:11.9

Name of the Organization	Designation	Date From	Date To
Sir C.R Reddy College of Engineering	Assistant professor	19-06-2010	Till date
Oil country tubular Ltd.	Engineer trainee	17-08-2006	15-03-2007

13. Courses taught:

S.No.	Name of the subject	UG/PG
1	VLSI Design & Embedded system	UG
2	Probability theory & Random process	UG

3	System design through Verilog	UG
4	Principles of economics & management	UG
5	VLSI design techniques.	PG
6	VHDL modelling of digital systems.	PG
7	Physical design and automation	PG

14. Administrative Responsibilities:

S.No.	Description	College/ Department level
1	Placement Co-ordinator member	Department level
2	EPABX incharge	College level
3	Counselling coordinator member	Department level

15. Research Guidance:

M.Tech.		Ph.D.	
Completed	Ongoing	Completed	Ongoing
completed			ongoing

16. Research Publications in Journals (National/International):

S.No.	Author(s) Name	Title of the Paper	Name of the Journal	National/ International	Month & Year	Volume , Issue No, Page Nos	UGC/ Web of Sciences / Scopus/ SCI/Others
1.	Ajaykumar Dharmireddy , Dr.Avinash Sharma , Sreenivasa Rao Ijjada	3D Analytical Modeling Of Surface Potential And Threshold Voltage Model Of Dm Fintfet With Dual Hetero Gate Oxide Structure	Turkish Journal of Computer and Mathematics Education	International	2020-21	1245-1259	Scopus
2	Ajaykumar Dharmireddy , Dr.Avinash Sharma , M.Sushanth Babu , Sreenivasa Rao Ijjada	SS<30mV/dec; Hybrid Tunnel FET 3D Analytical Model for IoT applications	Materials Today: Proceedings, (Elsevier – Article)	International	September, 2020, 2020-21	On going process	Scopus
3.	Ajay Kumar Dharmireddy, Sreenivasa Rao Ijjada, Chaithanya Mannepalli, K Shashidhar and Chakradhar Adupa	A Study of Land Terrain Classification Using Polarimetric SAR Images Based on DTC	Bioscience Biotechnology Research Communications	International	2020-21	Vol. 13, Issue spl - 2	Web of Sciences
4	Ajay kumar Dharmireddy, Anjani Devi N, Dr.Sreenivasa Rao Ijjada	Performance analysis of double gate hetro junction	IJITEE	International	2019-20	Volume-9 Issue-2S3	Scopus

		TFET					
4.	Dharmireddy Ajay Kumar ,P.V.T.Lokesh Kumar, P.H.S Tejomurthy	“Design of A NEMS Cantilever Sensor for Explosive Detection”	International Journal of Engineering and Advanced Technology (IJEAT)	International	2019-20	Vol.8, Issue-6S2	Scopus
5.	Ajay kumarDharmireddy, Anjani Devi N, Dr.Sreenivasa Rao Ijjada	Performance Characteristics of TFET over MOSFET, DGMOSFET and FinFET	TEST Engineering & Management, ISSN: 0193 - 4120	International	2019-20	Vol. 11, Issue spl - 2	Scopus
6.	Dharmireddy Ajay Kumar , Dr.Sreenivasa Rao Ijjada,P.H.S Tejomurthy	“Performance analysis of Tri-Gate SOI FINFET Structure with various fin heights using TCAD Simulation”	JARDCS	International	2018-19	Vol.11,spl.I Issue-2	Scopus
7.	D.Ajaykumar, P.Gopi Krishna K.srinivasa Rao P,Hareesh	Implementation of Bi-Directional Blue-Fi Gate Way in IOT Environment	IJSSIS(Scopus)	International	2017-2018	Vol.7, Issue-2	Scopus
8.	D.Ajaykumar, Sathyanarayana, Dr.Sreenivasa Rao Ijjada	Realization of Redundant Binary Multiplier with Modified	IJSCSEIT	International	2017-18	Vol.2,Issue -6	UGC

17. Research Papers in Conferences (National/International):

S.No.	Author(s) Name	Title of the Paper	Name of the Conference/Journal	National/ International	Month & Year	Page Nos
1	Ajay kumar Dharmireddy, K. V. Gayathri	Simulation model of SOI TFET for low power applications	2nd NATIONAL CONFERENCE ON "ARTIFICIAL INTELLIGENCE & MACHINE LEARNING"	National	16th - 17th JULY 2021	28
1.	D.AJAYKUMAR	Rad-Hard model SOI Fin TFET for space craft applications	ICMEET-2021, Springer series lecture notes.	International conference	27 th ,28 th August 2021	On going process.
2.	Ajay kumarDharmireddy, Anjani Devi N, Dr.Sreenivasa Rao Ijjada	Design and Analysis of Homo and Hetero junction	ICACCT-2021	International conference	6 th ,7 th may-2021	35

		Double gate TFET at 40nm For the IoT core Design				
3.	Ajay kumar Dharmireddy, Anjani Devi N, Dr.Sreenivasa Rao Ijjada	Design and performance analysis of Homo junction and Hetero junction Double gate TFET at 40nm	ICACCT-2021	International conference	6 th ,7 th may-2021	8
4	Ajaykumar Dharmireddy, Drsreenivasa Rao Ijjada	A perspective review of challenges on FinFET technology in low power & High speed design.	ICMEET-2020	International conference	6 th ,7 th Dec 2019	36

18. No of Books Published:

S.No.	Author(s) Name	Title	Publisher	ISBN	Year of Publishing
	nil				

19. Workshops/ FDPs/STTPs /etc., (Organised):

S.No.	Name of the Workshop/ FDP/STTP/ etc.,	Place	Period	
			From	To
1.	5G design: journey from devices to circuits	IIT, Guwahati	01-03-2021	12-03-2021
2	3-days workshop on "IOT using Raspberry Pi hands on approach	SIR CRRCOE, Eluru	13-02-2020	15-03-2020
3.	Guest lecture on "startups and latest technology awareness"	SIR CRRCOE, Eluru	20-12-2020	20-12-2020

20. Workshops/ FDPs/STTPs/ etc., (Attended):

S.No.	Name of the Workshop/ FDP/STTP/ etc.,	Place	Period	
			From	To
1.	MAU-HFSS-2021 workshop	IIT, Bhagalpur	27-01-2021	31-01-2021
2.	RISC-V VLSI implementation flow: RTL2GDS	IIT, Guwahati	27-03-2021	10-04-2021
3	5G design: journey from devices to circuits	IIT, Guwahati	01-03-2021	12-03-2021
4	Design challenges in deep sub-micron	Department of E.C.E,	5-08-2019	19-08-2019

	technology	Raghu institute of technology, Visakhapatnam		
5	Bio-medical signal processing & Applications.	Department of E.C.E, Sir C.R.Reddy College of engineering, Eluru.	1-11-2019	14-11-2019
4	NPTEL-AICTE FDP	IIT Madras	Aug 2018	Sep 2018
5.	VLSI & FPGA Implementation of Signal Processing Algorithms & Applications	Jointly Organized by Electronics & ICT Academy GITAM university, vishakapatnam	2-5-2018	7-5-2018

21. Sponsored Projects:

S.No.	Title	Sponsored by	Amount	Period	Ongoing/ Completed
	Nil				

22. Consultancy Projects:

S.No.	Title	Agency	Amount	Period	Ongoing/ Completed
	Nil				

23. No of Patents (Filed/Granted):

S.No.	Title	Application Number	Year	Filed/Granted
1	Live start-up circuit for bandgap reference (BGR)	202041052318A	2020	Filed

24. Awards/HonoursReceived:

S.No.	Award Name	Awarded by	Contribution	Date Received
	Nil			

25. Professional Memberships:

S.No.	Name of the Professional Body	Membership No.	Membership Type
1	IEEE member	97542166	Annual
2	MIE	M-1537789	Permanent
3	IAENG	134044	Permanent
4	IETE	M 500572	Permanent

26. Membership in BOS/ Editorial Boards:

S.No.	Name of the University/ Journal	Member/ Editor/ Reviewer	Period
	NIL		

27. Details of Personal Blogs/ Educational Youtube Channels/ e content (if any):

https://www.youtube.com/results?search_query=ajaykumar+dharmireddy

28. Details of Innovative Design/ Development of Product or Model (if any):

nil

29. Technology Transfer:

30. Special Achievements (If any):

- Ratified as Assistant Professor from Andhra University
- Ratified as Assistant Professor from JNT University, Kakinada.

31. Personal Details:

- (a) Father's Name : D.Ramachandra Rao
(b) Mother's Name :D.Umamaheswari.
(c) Gender : male
(d) Marital Status : married
(e) Religion : Hindu.
(f) Nationality : Indian



Signature