Name	TELANAKULA MALLESWARA RAG		No. of Concession, Name of Street, or other parts of the Concession, Name of Street, or other pa
Designation	MALLESWARA RAO		
Designation	ASSISTANT PROFESSOR		No ma
Department	ECE		7 -
Date of Birth	11/06/1980		TIME
Date of joining the Institution	12/08/2023		
Unique ID	1-4760036224		
	UG	PG	Ph.D.
Educational Qualifications with class/grade	B.Tech(ECE)	M.TECH (VLSI SD)	-
	second Class	First Class	
Work Experience	Teaching		11 YEARS
	Research		-
	Industry		-
	Others		-
Area of Specialization	VLSI		
Courses taught	UG Level		PG Level
	1.STLD 2.VLSI 3. DICA 4. EMBEDDED SYSTEMS 5. ERTS 6, SDD 7.BEEE 8.ECA 9.DLD		1.DDV 2.ERTS 3.VLSI 4.CPLD& FPGA
Research Guidance	Master's		Ph.D.
Projects carried out		-	•
Patents		-	
Technology Transfer		-	
Research Publications	International J	ournals	01
	National Journals		-
	International Conferences		01
	National Conferences		02
No. of Books published with details		-	
Achievements	1.3 RD Prize in Two Week Faculty Development Program on "Bio-Medical Signal Processing and Applications" Organized by SIR C R REDDY COE, Eluru.		