

FACULTY PROFILE

1. **Name** : Kollipara Radha
2. **Designation** : Assistant Professor
3. **Department** : ECE
4. **Date of Joining** : 03/08/2016
5. **Nature of Employment:** Regular
6. **Date of Birth** : 18/07/1978
7. **Unique ID (AICTE FID)** : 1-3196342667
8. **Email** : radha.gec@gmail.com
9. **Phone** : 8688310995
10. **Address for communication:** Flat No.102, Apple Homes, Gowthami Nagar,
Industrial Estate, Eluru (W.G.Dt.) – 534007



11. Educational Qualifications:

Qualification	Institute	University/Board	Specialization	Year of Passing	Division/Class
Ph.D.	JNTUK	JNTUK, Kakinada	VLSI Design	-	-
PG	Gudlavalleru Engineering College	JNT University (JNTUH)	Digital Electronics and Communication Systems	2008	First Class(Distinction)
UG	Institution of Engineers	Institution of Engineers (INDIA)	Electronics and Communication Engineering	2003	Second Class
Inter/Diploma	Govt.Polytechnic for Women	State Board of Technical Education & Training	Electronics and Communication Engineering	1996	First Class(Distinction)
SSC	Deepthi English Medium School	Board of Secondary Education	-	1993	First Class(Distinction)

12. Work Experience:

Teaching: **Research:** **Industry:** **Total:**

Name of the Organization	Designation	Date From	Date To
Sir.C.R.Reddy College of Engineering, Eluru	Assistant Professor	Aug 2016	To till date
Medha Institute of Science and Technology for women, Khammam	Assistant Professor	June 2014	July 2016
Gudlavalleru Engineering College, Gudlavalleru	Assistant Professor	Jun 2008	May 2014
A.A.N.M. & V.V.R.S.R. Polytechnic, Gudlavalleru	Lecturer	July 2004 Aug 2005	April 2005 Aug 2006

13. Courses taught:

S.No.	Name of the subject	UG/PG
1	Electronic Devices and Circuits	UG
2	Basic Electronic Devices and Circuits	UG
3	Electrical and Electronics Engineering	UG
4	Linear and Digital IC Applications	UG
5	Switching Theory and Logic Design	UG
6	VLSI Design	UG
7	Pulse and Digital circuits	UG
8	Microprocessors and Microcontrollers	UG
9	Analog Communication	UG
10	Digital logic design	UG
11	Linear IC Applications	UG
12	System Design through Verilog	UG
13	Embedded System Concepts	PG
14	Advanced Microcontrollers and Processors	PG
15	VLSI Technology and Design	PG
16	Digital System Design	PG
17	Advanced Microprocessors	PG
18	Telecommunication Switching Networks	PG

14. Administrative Responsibilities:

S.No.	Description	College/ Department level
1	Guest lecture coordinator	Department level
2	One of the co-coordinator for NAAC	Department level
3	Member in Criteria5 for NBA	Department level
4	M.Tech.Project review member	Department level
5	Class teacher	Department level
6	Student counsellor	Department level
7	Antiragging duty	College level

15. Research Guidance:

M.Tech.		Ph.D.	
Completed	Ongoing	Completed	Ongoing
15	1	-	-

16. Research Publications in Journals (National/International):

S.No.	Author(s) Name	Title of the Paper	Name of the Journal	National/ International	Month & Year	Volume, Issue No, Page Nos	UGC/ Web of Science s/ Scopus / SCI/Others
1	Raviraja.Peddiseti,	Modified Positive	IJR	International	June	Vol.8,	Others

	Radha.Kollipara	Feedback Adiabatic Logic based low power Multiplexer and Comparator			2018-19	Issue 6, pp.-3948-3958	
2	K.Radha,V.Swathi et. al	Implementation of low power adders and parity circuits using Gate Diffusion Input technique	IJMTE	International	June 2018-19	Vol.9, Issue 6, pp.-3766-3780	Others
3	Kodavali Bhargav Kumar, K.Radha	High Speed Aes S-Box/Inv S-Box Design With S.R And M.C Technique”	IJR	International	July 2017-18	Vol.5, Issue 17, pp.399-404	Others
4	M.Suresh, K.Radha	Design of RRAM based Non-volatile lookup table in FPGA	IJRECE	International	Oct 2017	Vol.5, Issue 4, pp.288 – 291	UGC
5	P.Ratna Spandana, K.Radha, K.Miranji	Design of CMOS based PLC Receiver	IJMTST	International	Oct 2017	Vol.3, Issue 10, pp.138 – 144	Others
6	K.Radha, R.Anila, Dr. Murali Malijeddi	Power is calculated using distributed arithmetic and simulated using Xilinx ISE simulator	IJR	International	June 2016	Vol.3, Issue 10, pp.75 – 80	Others
7	K.Radha, K.Anusha, Dr. Murali Malijeddi	Data path synthesis arithmetic optimizations flexible accelerator operation chaining	IJR	International	June 2016	Vol.3, Issue 10, pp.70 – 75	Others
8	Radha.K, M. Sowmyasri, N.Harshala, B.Neeraja	Self – Controllable Voltage Level Technique to reduce leakage current in DRAM 4X4	IJERA	International	Jan2016	Vol.6, Issue 01, pp.85 – 90	Others
9	Boggarapu Kantha Rao, K.Radha, Penti Supraja	Realization of Data Encoding Schemes in Network on Chip using Verilog HDL	IJR	International	June 2015	Vol.2, Issue 06, pp.747 – 755	Others
10	K.Radha, B.Kantha Rao, K.Greeshma	Elder People Caring System using Sensor Network”	IJSETR	International	June 2015	Vol.4, Issue 17, pp.3120 – 3125	Others
11	K.Radha, B.Kantha Rao, G.Rani	WOT for Energy Management with Smart Grid Connecting	IJAEGT	International	July 2015	Vol.3, Issue 7, pp.950 – 954	Others
12	T.Sairam Vamsi, K.Radha	ARM Based Stair Climbing Robot Controlling Through DTMF Technology	IJRTE	International	July 2013	Vol.2, Issue 3, pp. 71-74	Others
13	P.Ramya, K. Radha	Design of Efficient Transport Protocol for Multimedia through Wireless Networks”	IJRTE	International	July 2013	Vol.2, Issue 3, pp. 55-58	Others
14	V. Hari Prasad, Ashok Kumar Yadav, K. Radha	Porting of Android OS with BSP Customization on NXP2120 Application Processor Platform	IJVES	International	Sep.2013	Vol.4, Article 08146, pp.592-98	Others
15	Ch.L.N.Bhavya Sri,	An advance	IJESR	International	Oct.2013	Vol.3,	Others

	K.Radha,	technology in automobiles by using Matlab PCA algorithm				Issue 10, pp.4821 – 4829	
16	P.Raghu, K.Radha	AMBA bus based system on chip communication using AXI4 protocol	IJVES	International	Apr.2013	Vol.4, Issue 2, pp.289-292	Others

17. Research Papers in Conferences (National/International):

S.No.	Author(s) Name	Title of the Paper	Name of the Conference /Journal	National/ International	Month & Year	Page Nos
1	K. Radha, S. Gayathri, T. Hemalatha, V. Akhil, S. Kartheek Reddy, Y. Teja	Adiabatic based 4 bit comparator for low power applications	Artificial Intelligence & Machine Learning	National	July,2021	19
2	K. Radha, N. Triveni, T. Lavanya, P. Bharathi, P. Pallavi, J. Tejesh	Design of adders using Gate Diffusion Input technique	Artificial Intelligence & Machine Learning	National	July,2021	29
3	Greeshma.M, Lavanya.K, K.Radha, Aneela.Patchigolla	High speed and low power comparator for high speed ADCs	ICACT-2019	International	April, 2019	61-64
4	K.Lavanya, K.Radha, S.V.R.Rajeswari, B.G.S.Meenakshi	Design and analysis of low power SRAM cells	ICACT-2019	International	April, 2019	177-182

18. No of Books Published:

S.No.	Author(s) Name	Title	Publisher	ISBN	Year of Publishing
	-	-	-	-	-

19. Workshops/ FDPs/STTPs /etc., (Organised):

S.No.	Name of the Workshop/ FDP/STTP/ etc.,	Place	Period	
			From	To
1	“IOT in Smart Cities” Guest lecture	Sir.C.R.Reddy College of Engineering	5/01/2018	5/01/2018
2				

20. Workshops/ FDPs/STTPs/ etc., (Attended):

S.No	Name of the Workshop/ FDP/STTP/ etc.,	Place	Period	
			From	To
1	5-day online FDP on the theme "Inculcating Universal Human Values in Technical Education"	Online: All India Council for Technical Education(AICTE)	28/06/2021	2/07/2021
2	Xilinx webinar	Online : CoreEL Technologies and Xilinx	22/05/20	-
3	5 day Online FDP : Trends in communication"	Online :Dept.of ECE,Sir CRR COE	26/05/20	30/05/20
4	3 days Online National level FDP:" Tools for online classroom post covid-19"	Online : P.B.SC of Arts & Science and Computer society of India	18/05/20	20/5/20
5	Webinar "The need for change in pedagogy and a simple way to teach online with a Smartphone for beginners"	Online : Usharama College of Engineering and Technology	11/05/20	-
6	1-day project based training program" Introduction to Arduino"	Online : Skyfi labs	12/05/20	-
7	5 days Online certification skill development program" Digital system design with verilog and vivado"	Online : Sandeepani school of embedded system design, CoreEL technologies	11/05/20	15/05/20
8	3 day online FDP" Advanced sensors and its applications"	Online: LBRCE	1/05/20	3/5/20
9	One week Virtual classroom training" ASIC Physical Design and Verification"	Online: EXCEL VLSI Technologies	25/04/20	30/04/20
10	One week Virtual classroom training" ASIC Synthesis and Static Timing Analysis"	Online: EXCEL VLSI Technologies	20/4/20	25/4/20
11	Coursera : Online non-credit course" Internet of things: Multimedia technologies"	Online: UC San Diego	29/4/20	-
12	Coursera : Online non-credit course" Programming for everybody (Getting started with Python)"	Online : University of Michigan	26/4/20	-
13	2 days FDP "Multi carrier modulation in broadband communication and technology challenges in 5G : IETE:IEEE"	Online : Raghu Engineering College	29/5/20	30/5/20
14	NPTEL Course: "Introduction to Internet of things"	IIT Kharagpur	27/01/ 2020	17/04/ 2020
15	Webinar" Reconfigurable Antennas"	Online : NEC, Narasaraopet	26/5/20	-
16	Two week FDP : AICTE sponsored" Bio-medical signal processing and applications"	SIR C R R COE	1/11/19	14/11/19

17	NPTEL Course: " CMOS Digital VLSI Design"	IIT Roorkee	25/02/2019	19/04/20
18	NPTEL Course: " Digital Circuits"	IIT Kharagpur	29/07 2019	18/10/ 20
19	One week National Level FDP on Recent Trends in VLSI Design and its Applications	SRKR Bhimavaram	20/11/2017	25/11/20
20	Two day workshop - FPGA based system design using Verilog	C.R.Reddy College of Engineering	6/10/17	7/10/20
21	National workshop on SPACE DYNAMICS (NWSD)	Sir.C.R.Reddy College (Aided & Autonomous)	4/10/2017	6/10/20
22	One day program -Tech gen Y	C.R.Reddy College of Engineering	1/04/2017	1/04/20
23	One day National Symposium -Internet of Things	C.R.Reddy College of Engineering	17/09/2016	17/09/20
24	Two day workshop -3D modelling of Electromagnetic systems using ANSYS-RF tools	C.R.Reddy College of Engineering	9/09/2016	10/09/20
25	Two day workshop -MATLAB for Engineering Applications	C.R.Reddy College of Engineering	19/08/2016	20/08/20
26	Two day workshop -IC Design Flow using Mentor Graphics Tools	C.R.Reddy College of Engineering	05/08/2016	06/08/20
27	Faculty Enablement Program -Organization Impact Skills and Art of Facilitating Behavioral Skills	KITS, Khammam	16/11/ 2015	8/11/20
28	Two day workshop -Issues in integrated Circuits Design	GEC, Gudlavalleru	13/12/2013	14/12/ 20
29	One week refresher course -EDC (Theory & Lab practice)	GEC, Gudlavalleru	23/09/2013	28/09/.20
30	Two week ISTE workshop -Analog Electronics	GEC, Gudlavalleru	4/06/2013	14/06/ 20
31	One week workshop -Training teachers for excellence	GEC, Gudlavalleru	29/04/2013	4/04/20
32	Two day Orientation -MATLAB, Simulink & Related tool boxes for Engineering Education	GEC, Gudlavalleru	3/04/2013	4/04/ 20
33	Two day workshop -Research Methodologies	GEC, Gudlavalleru	2/02/2013 and 9/02/2013	-
34	Two day workshop -Akash for Education	GEC, Gudlavalleru	10/11/2012	12/11/20
35	Faculty development program -VLSI and Embedded System Design	GEC, Gudlavalleru	7/07/2012	8/07/20
36	One week refresher course -Pulse and Digital Circuits	GEC, Gudlavalleru	21/11/2011	26/11/ 20
37	Five day training program -Mentor Graphics and XILINX tools	GEC, Gudlavalleru	13/09/2010	17/09/ 20
38	Two day National workshop -Pedagogy	GEC, Gudlavalleru	30/01/2009	31/01/20
39	Workshop on "Space Dynamics"	Sir C R Reddy Autonomous College	04-10-17	06-10-17

21. Sponsored Projects:

S.No.	Title	Sponsored by	Amount	Period	Ongoing/ Completed
	-	-	-	-	-

22. Consultancy Projects:

S.No.	Title	Agency	Amount	Period	Ongoing/ Completed
	-	-	-	-	-

23. No of Patents (Filed/Granted):

S.No.	Title	Application Number	Year	Filed/Granted
	-	-	-	-

24. Awards/Honours Received:

S.No.	Award Name	Awarded by	Contribution	Date Received
	-	-	-	-

25. Professional Memberships:

S.No.	Name of the Professional Body	Membership No.	Membership Type
1	MIETE	M-500578	Member, Lifetime

26. Membership in BOS/ Editorial Boards:

S.No.	Name of the University/ Journal	Member/ Editor/ Reviewer	Period
	-	-	-

27. Details of Personal Blogs/ Educational Youtube Channels/ e content (if any):

e content :

1. Subject : MPMC

You tube link : https://youtube.com/playlist?list=PLUEnStKU07jJb-nd9bs_erP106fLgYrGo

2. Subject : SDTV

You tube link : <https://youtube.com/playlist?list=PL67Nrt50PIWki7AOj-2q0FzBFdgCGrVU3>

28. Details of Innovative Design/ Development of Product or Model (if any):

Nil

29. Technology Transfer: Nil

30. Special Achievements (If any): Nil

31. Personal Details:

(a) Father's Name : K.Rama Krishna Prasad
(b) Mother's Name : K.Rama Kumari
(c) Gender : Female
(d) Marital Status : Married
(e) Religion : Hindu
(f) Nationality : Indian

A handwritten signature in purple ink, appearing to read 'K. Ansha', with a horizontal line underneath the name.

Signature