

FACULTY PROFILE

1. **Name** :SURENDRA BABU VELAGALETI
2. **Designation** : ASSISTANT PROFESSOR
3. **Department** : E C E
4. **Date of Joining** : 03/06/2011
5. **Nature of Employment** : Regular
6. **Date of Birth** : 10/04/1985
7. **Unique ID (AICTE FID)** :
8. **Email** : vsb.mtech14@gmail.com
9. **Phone** : 9985911248
10. **Address for communication:**



11. Educational Qualifications:

Qualification	Institute	University/Board	Specialization	Year of Passing	Division/Class
Ph.D.	KL University	KL University	MEMS	---	---
PG	JNTUK	Sasi institute of Technology and Engineering Tadepalligudem	VLSI&ES	2010	I
UG	JNTUH	DVR&Dr.HS MIC College of Technology Kanchikacherla	E.C.E	2006	II
Inter/Diploma	Board Of Intermediate	APSWRJr.College, Nuzvid	M.P.C	2002	I
SSC	Board of SSC	Z.P.H.School Kanchikacherla	-----	2000	I

12. Work Experience:

Teaching: 12.1

Research:

Industry:

Total: 12.1

Name of the Organization	Designation	Date From	Date To
VKR&VNB Polytechnic	Lecturer	07/12/2006	30/06/2008
Sir C R Reddy College Of Engineering	Assistant Professor	03/06/2011	Till Date

13. Courses taught:

S.No.	Name of the subject	UG/PG
01	Electronic Devices and Circuits	UG

02	Pulse and Digital Circuits	UG
03	EMW &TL	UG
04	Digital Communications	UG
05	Digital Signal Processing	UG
06	Digital Image Processing	UG
07	Analog IC Design	PG
08	Digital IC Design	PG
09	System on Chip	PG
10	Embedded Systems	PG
11	VHDL Modelling of Digital Systems	PG

14. Administrative Responsibilities:

S.No.	Description	College/ Department level
01	Internal Exams In charge	Department level
02	Result Analysis member	Department level
03	Bus In charge	Department level
04	NBA MEMBER	Department level

15. Research Guidance:

M.Tech.		Ph.D.	
Completed	Ongoing	Completed	Ongoing
05	0		

16. Research Publications in Journals (National/International):

S.No.	Author(s) Name	Title of the Paper	Name of the Journal	National/ International	Month & Year	Volume, Issue No, Page Nos	UGC/ Web of Sciences/ Scopus/ SCI/Others
01	V Surendra Babu	Design of 8-bit Ripple Carry Adder using Constant Delay Logic	IJEECS	International	2014	2,8,9	others
02	v.Surendra Babu,A.Ram Kumar, ShaikMubeena	Implementation of triple Data Encryption standard Architecture	IJSRCSEIT	International	2017-18	2456-3307,4.032.	Others
03	V surendra babu,Miranji Katta, R, Ch JayaPrakash M Narendra Kumar ,vamsi krishna	Comparative analysis of lead-free piezoelectric material for Ultrasonic glucose sensing applications	Jour of Adv Research in Dynamical & Control Systems, , 2019	International	2019 APRIL	Vol. 11,2,1110-1116	SCOPUS

17. Research Papers in Conferences (National/International):

S.No.	Author(s) Name	Title of the Paper	Name of the Conference/Journal	National/International	Month & Year	Page Nos
01	V Surendra Babu	Vlsi Architecture For Error Free Nano Memory Systems	ICCIEA	International	2012	
02	V Surendra Babu	Design Of 8-Bit Ripple Carry Adder Using Constant Delay Logic	ICEECE	International	2014	
03	V Surendra Babu	Design And Analysis Of 4- Bit Shift Register Using Low Power Pulse Triggered Flip Flop	NCRRECE	National	2014	
04	V. Surendra Babu, P. Dhana Lakshmi, K. L. V. Sai Charan, M. Tejaswi, P. Sagar, K. Darren Hons	Design and Implementation of the Logic Circuits Using Low Power Adiabatic Logic Technique	2nd NATIONAL CONFERENCE ON "ARTIFICIAL INTELLIGENCE & MACHINE LEARNING"	National	16th - 17th JULY 2021	25
5	V Surendra Babu	Opac based scader system in visakapatnam steel plant	ICACT-2019	INTERNATIONAL	2019	165

18. No of Books Published:

S.No.	Author(s) Name	Title	Publisher	ISBN	Year of Publishing

19. Workshops/ FDPs/STTPs /etc., (Organised):

S.No.	Name of the Workshop/ FDP/STTP/ etc.,	Place	Period	
			From	To

20. Workshops/ FDPs/STTPs/ etc., (Attended):

S.No.	Name of the Workshop/ FDP/STTP/ etc.,	Place	Period	
			From	To
01	Soft Computing And Signal Processing Applications To Engineering	Sasi Institute Of Technology And Engineering, Tadepalligudem	24/06/2011	25/06/2011
02	VLSI Design Using Mentor Graphics Tools	Sri Vishnu Engineering College For Women ,Bhimavaram	23/08/2012	24/08/2012
03	PLC Programming And Applications	Sir C R Reddy College Of Engineering, Eluru	01/03/2015	01/03/2015
04	IC Design Flow Using Mentor Graphics Eda Tools	Sir C R Reddy College Of Engineering, Eluru	05/08/2016	06/08/2016
05	3D Modelling Of Electromagnetic Systems Using Ansys Rf –Tools	Sir C R Reddy College Of Engineering, Eluru	09/09/2016	10/09/2016
06	Space Dynamics	Sir C R Reddy College (Autonomous), Eluru	04/10/2017	06/10/2017
07	FDP On CMOS Analog IC Design	VR Siddhartha Engineering College	13/11/2017	18/11/2017
08	"Research Trends in Signal Processing, Antennae, VLSI & IoT"	Sri Vasavi Engineering College(Autonomous) Tadepalligudem	28/05/2020	30/05/2020

21. Sponsored Projects:

S.No.	Title	Sponsored by	Amount	Period	Ongoing/ Completed

22. Consultancy Projects:

S.No.	Title	Agency	Amount	Period	Ongoing/ Completed

23. No of Patents (Filed/Granted):

S.No.	Title	Application Number	Year	Filed/Granted

24. Awards/Honours Received:

S.No.	Award Name	Awarded by	Contribution	Date Received

25. Professional Memberships:

S.No.	Name of the Professional Body	Membership No.	Membership Type

26. Membership in BOS/ Editorial Boards:

S.No.	Name of the University/ Journal	Member/ Editor/ Reviewer	Period

27. Details of Personal Blogs/ Educational Youtube Channels/ e content (if any):

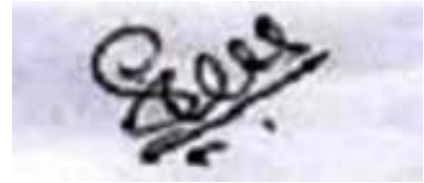
<https://velagaleti9985.blogspot.com/>

28. Details of Innovative Design/ Development of Product or Model (if any):**29. Technology Transfer:****30. Special Achievements (If any):****31. Personal Details:**

- (a) Father's Name : PEDA PAMULU
(b) Mother's Name : SARAMMA
(c) Gender : Male
(d) Marital Status : Married
(e) Religion : Hindu

(f) Nationality

: Indian

A handwritten signature in black ink on a light, textured background. The signature is written in a cursive style and appears to be 'S. S. S.' with a horizontal line underneath the letters.

Signature