

SIR C. R. REDDY COLLEGE OF ENGINEERING, ELURU-07  
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING  
III/IV BE (ECE) - II Sem, Time Table, Sec - A, A.Y 2019-20

Class Teacher : Mr. D. Ajaykumar

Dt: 16/11/2019  
w.e.f: 18/11/2019

LH-24

Periods	1	2	BREAK	3	4	LUNCH	5	6	7	8
Theory	9.00-09.50	9.50-10.40		11.00-11.50	11.50-12.40		01.40-02.30	02.30-03.20	03.20-04.10	04.10-05.00
MON	VLSID	BME		ME	MPMC		VLSID / MP LAB			
TUE	DSP	MPMC		VLSID	BME		DC / MP LAB			
WED	VLSID / DC LAB 8.10AM TO 10.40AM			MPMC	DSP		VLSID	BME	DSP	
THU	MPMC	ME		DSP	ME		STP			
FRI	ME	DSP		BME	VLSID		STP			
SAT	BME	VLSID		MPMC	ME					

Examinations in-charge: Mr. A. Vamsi Krishna

Attendance in-charge: Mrs. M. Greeshma

Code	Subject	Staff
1	Microprocessors & Microcontrollers	Mr. A. Vamsi Krishna
2	Microwave Engineering	Mrs. P. Soumithri
3	VLSI Design	Mr. D. Ajay Kumar
4	Digital Signal Processing	Mr. A. Ramkumar
5	Bio-Medical Engineering	Mrs. M. Greeshma
6	Microprocessors & Microcontrollers Lab	AVK / ARK
7	VLSI Design Lab	DAK / MG
8	Digital Communications Lab	NSW / VVS

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Time Table / C

*[Signature]*  
**HOD-ECE**  
Head of the Department  
Electronics & Communication Engg  
Sir C.R.R. College of Engineering  
Eluru - 534 007

*[Signature]*  
**PRINCIPAL**  
Principal  
SIR C.R.R. COLLEGE OF ENGINEERING  
ELURU - 534 007.

**SIR C. R. REDDY COLLEGE OF ENGINEERING, ELURU-07**  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
**III/IV BE (ECE) - II Sem, Time Table, Sec - B, A.Y 2019-20**

Class Teacher : Dr. M. Ravikumar

Dt: 16/11/2019  
w.e.f: 18/11/2019

LH-25

Periods	1	2	BREAK	3	4	LUNCH	5	6	7	8
Theory	9.00-09.50	9.50-10.40		11.00-11.50	11.50-12.40		01.40-02.30	02.30-03.20	03.20-04.10	04.10-05.00
MON	DSP	ME	LUNCH	VLSID	MPMC	CRT TRAINING				
TUE	VLSID	DSP		BME	ME	CRT TRAINING				
WED	MPMC	ME		VLSID / MP LAB 10.40AM TO 1.10PM		CRT TRAINING				
THU	MPMC	VLSID / DC LAB			BME	VLSID	ME			
FRI	DSP	MPMC			BME	VLSID	STP			
SAT	MP / DC LAB 8-10AM to 10.40AM			DSP	BME					

Examinations in-charge: Mr. CH. Ravikumar

Attendance in-charge: Mr. V. Veerraju

Code

Subject

Staff

1	Microprocessors & Microcontrollers	Mrs. N. Swathi
2	Microwave Engineering	Mr. K. Lashminarayana
3	VLSI Design	Mr. P. Hareesh
4	Digital Signal Processing	Dr. P. H. S. Tejomurthy
5	Bio-Medical Engineering	Dr. M. Ravikumar
6	Microprocessors & Microcontrollers Lab	Dr. MRK / NSW
7	VLSI Design Lab	PH / MG
8	Digital Communications Lab	KLN / VS

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Time Table / CC

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**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
**III/IV BE (ECE) - II Sem, Time Table, Sec - C, A.Y 2019-20**

Class Teacher : Ms. I. Hemalatha

LH-26

Dt: 16/11/2019  
w.e.f: 18/11/2019

Periods	1	2	BREAK	3	4	LUNCH	5	6	7	8
Theory	9.00-09.50	9.50-10.40		11.00-11.50	11.50-12.40		01.40-02.30	02.30-03.20	03.20-04.10	04.10-05.00
MON	MPMC	OOPS		ME	VLSID		CRT TRAINING			
TUE	DSP	VLSID / DC LAB					CRT TRAINING			
WED	VLSID	MPMC		ME	OOPS		CRT TRAINING			
THU	MPMC	OOPS		ME	VLSID		DSP	MP / DC LAB		
FRI	DSP	VLSID / MP LAB					VLSID	STP		
SAT	OOPS	ME		MPMC	DSP					

Examinations in-charge: Mrs. N. Lavanya

Attendance in-charge: Mrs. V. Swathi

Code	Subject	Staff
1	Microprocessors & Microcontrollers	Mr. K. Miranji
2	Microwave Engineering	Dr. T. Venkateswara Rao
3	VLSI Design	Mr. R. Trinath
4	Digital Signal Processing	Ms. I. Hemalatha
5	OOPs Through Java	Dr. CH. Madhava Rao
6	Microprocessors & Microcontrollers Lab	CHMR / KM
7	VLSI Design Lab	RT / KL
8	Digital Communications Lab	IH / PS

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Time Table I/C

*[Signature]*  
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 Sir C.R.R. College of Engineering  
 Eluru - 534 007

*[Signature]*  
**PRINCIPAL**  
 Principal  
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**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
**III/IV BE (ECE) - II Sem, Time Table, Sec - D, A.Y 2019-20**

Class Teacher : Dr. CH. Madhava Rao

Dt: 16/11/2019  
w.e.f: 18/11/2019

LH-27

Periods	1	2		3	4		5	6	7	8		
Theory	9.00-09.50	9.50-10.40		11.00-11.50	11.50-12.40		01.40-02.30	02.30-03.20	03.20-04.10	04.10-05.00		
MON	VLSID	VLSID / DC LAB				LUNCH	CRT TRAINING					
TUE	ME	DSP	BREAK	OOPS	MPMC		CRT TRAINING					
WED	MPMC	OOPS		VLSID	ME		CRT TRAINING					
THU	ME	VLSID		MPMC	OOPS		STP					
FRI	OOPS	DSP		MPMC	ME		DSP	MP / DC LAB				
SAT	DSP	VLSID		VLSID / MP 10.40 AM to 1.10 PM								

Examinations in-charge: Mr. CH. Jayaprakash

Attendance in-charge: Mr. K. Lakshminarayana

Code	Subject	Staff
1	Microprocessors & Microcontrollers	Mrs. K. Radha
2	Microwave Engineering	Mr. K. Lakshminarayana
3	VLSI Design	Mr. R. Trinath
4	Digital Signal Processing	Mrs. V. V. Sudheera
5	OOPs Through Java	Dr. CH. Madhava Rao
6	Microprocessors & Microcontrollers Lab	KR / MNK
7	VLSI Design Lab	ARK / CHJP
8	Digital Communications Lab	VVS / NS

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Time Table I/C

*[Signature]*  
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Sir C.R.R. College of Engineering  
Eluru - 534 007

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Principal  
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